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		Application Number	TBD 10/697,137			
		Filing Date	10/30/2003			
		First Named Inventor	Jeffrey A. West			
		Group Art Unit	TBD			
		Examiner Name	TBD			
Sheet	1	of	1	Attorney Docket No.	TI-36238	

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS					
Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
MG	CA	T. TAKEWAKI et al., "Excellent Electro/Stress-Migration-Resistance Giant-Grain Copper Interconnect Technology for High-Performance Power Devices," Proceedings of 1995 International Symposium on Power Semiconductor Devices & ICs, Yokohama, pp. 438-442.			
MG	CB	S. HYMES et al., "Passivation of copper by silicide formation in dilute silane," J. Appl. Phys. 71 (9), 1 May 1992, pp. 4623-4625.			
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9-27-04